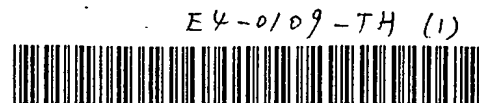


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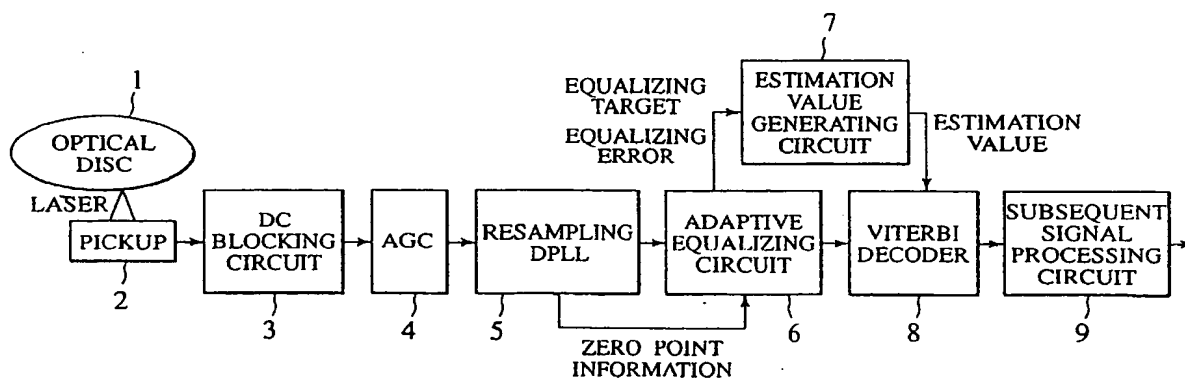
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**(54) Reproducing apparatus for optical disc recorded in high density**

(57) A resampling DPLL (5) estimates a signal outputted from a pickup (1) through an AGC (4) and outputs a signal of the estimation result. A transversal filter in an adaptive equalizing circuit (6) equalizes in waveform the

signal outputted from the resampling DPLL (5). An error signal is detected from the equalized waveform outputted from the transversal filter. The error signal is fed back to the transversal filter so as to control the transversal filter such that the error signal becomes minimum.

**FIG.1****EP 1 160 785 A2**

## Description

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

[0001] The present invention relates to an optical disc reproducing apparatus for reproducing digital information from an optical disc medium in which the digital information is modulated and recorded in a recording track formed coaxially or spirally according to signal modulation method for restricting run length for limiting the recording mark length, and more particularly to an optical disc reproducing apparatus having a laser of 390 to 440 nm in wavelength for reproducing information from a high density read only memory (ROM) or from rewritable disc in which the information is recorded such that a recording mark in the optical disc medium is 0.32 $\mu$ m or more to less than 0.38 $\mu$ m in track pitch and 0.12 $\mu$ m or more to less than 0.22 $\mu$ m in mark length.

#### 2. Description of the Related Art

[0002] Since before, as a means for reproducing information from a high-density ROM disc, a reproducing apparatus provided with a combination of a partial response equalization and viterbi decoding using run length restriction in recording modulation has been disclosed in, for example, Pioneer R&D Vol. 6. No.2 "Development of Viterbi Decoder for DVD" and Japanese Patent Application Laid-Open No. H10-106161.

[0003] Although prior arts disclosed in these document and publication contribute largely to a reproducing apparatus for reproducing from a medium having a recording density of DVD or similar capacity, in a reproducing apparatus for reproducing from a higher density ROM (read only) disc (hereinafter sometimes referred to as simply ROM disc), nonlinear distortion caused in pit formation and cross-talk between adjacent tracks are increased tremendously, so that no sufficient margin can be secured due to disc tilt and the like, thereby making it impossible to construct any reproducing apparatus. Further, the rewritable disc has a similar problem caused by a recording power deviation or the like.

### SUMMARY OF THE INVENTION

[0004] To solve the above described problem, the present invention intends to provide an optical disc reproducing apparatus capable of suppressing nonlinear distortion caused by pit formation, an influence by cross-talk between adjacent tracks, an influence by disc tilt and the like to a minimum extent in a high-recording density ROM disc in which a recording mark is 0.32 $\mu$ m or more to less than 0.38 $\mu$ m in track pitch and 0.12 $\mu$ m or more to less than 0.22 $\mu$ m in mark length, the apparatus using laser beam of 390 to 440 nm in wavelength.

[0005] 0.22 $\mu$ m and 0.38 $\mu$ m which are upper limits of

the length dimension and track pitch respectively are specified as values allowing recording of information with practically sufficient capacity for an optical disc medium of 12 cm in diameter. On the other hand, 0.12 $\mu$ m is a lower limit of the length dimension and is specified as a value within its practical limit not to increase jitters rapidly due to thermal interference between adjacent marks. Further, 0.32 $\mu$ m, which is a lower limit of the track pitch is a value specified by manufacturing limit for the disc.

[0006] To achieve the above object, there is provided an optical disc reproducing apparatus for reproducing digital information from an optical disc medium in which the information is modulated and recorded in a recording track formed coaxially or spirally according to a signal modulation method for restricting run length such that a recording mark is 0.32 $\mu$ m or more to less than 0.38 $\mu$ m in track pitch and 0.12 $\mu$ m or more to less than 0.22 $\mu$ m in mark length, comprising: laser beam source for emitting laser beam having wavelength of 390 to 440 nm; a reflection light detecting means for detecting a reflection light produced when the laser beam emitted from the laser beam source is reflected by the optical disc medium to read a signal from the optical disc medium, so as to reproduce the information recorded in the optical disc medium; a digital phase synchronizing means for receiving a supply of signal outputted from the reflection light detecting means and synchronizing digital phase so as to detect a phase at a data point and estimate digital data at the data point and outputting the digital data; an adaptive equalizing means including a transversal filter for equalizing in terms of waveform, the digital data outputted from the digital phase synchronizing means based on a filter coefficient, a temporary determining/subtracting circuit for while a sample after the waveform equalization outputted from the transversal filter is inputted therein, computing a temporary determining value for partial response equalization by comparing the sample with at least one threshold and outputting a differential between the temporary determining value and the sample after the waveform equalization as an error signal, and a coefficient control circuit for variably controlling a tap coefficient of the transversal filter so that the error signal becomes minimum; and a viterbi decoding means for viterbi-decoding a sample outputted from the adaptive equalizing means.

[0007] According to a preferred embodiment of the present invention, the coefficient control circuit includes an error selecting circuit for while the error signal and the temporary determining value outputted from the temporary determining means are inputted therein, selecting and outputting only valid component of the error signal depending on the temporary determining value, and a multiplier/low-pass-filter for variably controlling the tap coefficient of the transversal filter based on a signal outputted from the error selecting means such that the error signal becomes minimum.

[0008] According to another preferred embodiment of

the present invention, an estimation value generating means for generating an estimation value by averaging output sample of the transversal filter for each of the at least one threshold based on the temporary determining value and the error signal outputted from the temporary determining/subtracting circuit is further comprised.

[0009] According to still another preferred embodiment of the present invention, the viterbi decoding means includes an input means for an estimation value for use in metric operation, the estimation value being capable of being controlled through the input means.

[0010] According to yet still another preferred embodiment of the present invention, the estimation value for the viterbi decoding means is provided by the estimation value generating means.

[0011] To achieve the above object, there is provided an optical disc reproducing apparatus for reproducing digital information from an optical disc medium in which the information is modulated and recorded in a recording track formed coaxially or spirally according to a signal modulation method for restricting run length such that a recording mark is  $0.32\mu\text{m}$  or more to less than  $0.38\mu\text{m}$  in track pitch and  $0.12\mu\text{m}$  or more to less than  $0.22\mu\text{m}$  in mark length, comprising: laser beam source for emitting laser beam having wavelength of 390 to 440 nm; a first reflection light detecting means for detecting a reflection light produced when the laser beam emitted from the laser beam source is reflected by the optical disc medium to read a signal from the optical disc medium, so as to reproduce the information recorded in the optical disc medium; a second reflection light detecting means for reading information separately from each of at least one pair of recording tracks on both sides of the recording track; a digital phase synchronizing means for while a signal outputted from the first reflection light detecting means is supplied thereto, digitally synchronizing phase so as to detect a phase at a data point and estimate digital data at the data point and outputting the digital data; a resampling means for while a signal outputted from the second reflection light detecting means is supplied thereto, resampling a signal from at least one recording track of the at least one pair of the recording tracks at the data point in the digital phase synchronizing means; an adaptive equalizing means including a first transversal filter for equalizing in terms of waveform, the digital data outputted from the digital phase synchronizing means based on a first filter coefficient, a temporary determining/subtracting circuit for while a sample after the waveform equalization outputted from the first transversal filter is inputted therein, computing a temporary determining value for partial response equalization by comparing the sample with at least one threshold and outputting a differential between the temporary determining value and the sample after the waveform equalization as an error signal, a second transversal filter for equalizing in waveform, the digital data outputted from the resampling means based on a second filter coefficient and generating a pseudo cross-talk signal based

on an inputted tap coefficient, a subtracting means for subtracting the pseudo cross-talk signal from the sample after the waveform equalization, and a coefficient control circuit for variably controlling the tap coefficient inputted to the first and second transversal filters so that the error signal becomes minimum; and a viterbi decoding means for viterbi-decoding a sample outputted from the adaptive equalizing means.

[0012] According to a preferred embodiment of the present invention, the first coefficient control circuit includes an error selecting circuit for while the error signal and the temporary determining value outputted from the temporary determining means are inputted therein, selecting and outputting only valid component of the error signal depending on the temporary determining value, and a multiplier/low-pass-filter for variably controlling the tap coefficient of the first and second transversal filters based on a signal outputted from the error selecting means such that the error signal becomes minimum.

[0013] The nature, principle and utility of the invention will become more apparent from the following detailed description when read in conjunction with the accompanying drawings.

## BRIEF DESCRIPTION OF THE DRAWINGS

[0014] In the accompanying drawings:

Fig. 1 is a block diagram of an optical disc reproducing apparatus according to a first embodiment of the present invention;

Fig.2 is a block diagram of an adaptive equalizing circuit of the first embodiment of the present invention;

Fig.3 is a diagram for explaining a temporary determining operation and error selecting operation;

Fig. 4 is a block diagram of an example of concrete circuit configuration for an estimation value generating circuit and a viterbi decoder;

Fig.5 is a histogram of samples of data point at the time of reproduction or a histogram without selection of an error;

Fig.6 is a histogram of samples of data point at the time of reproduction or a histogram with selection of an error;

Fig.7 is a diagram showing radial tilt characteristic; Fig.8 is a diagram showing tangential tilt characteristic;

Fig.9 is a block diagram of an adaptive equalizing circuit according to a second embodiment of the present invention; and

Figs.10A-10C are diagrams for explaining an operation of the second embodiment of the present invention.

## DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0015] Hereinafter, the preferred embodiments of the present invention will be described with reference to the accompanying drawings. Fig.1 is a block diagram showing a reproducing apparatus for optical disc of the present invention. As for the dimension of a recording mark in the optical disc 1, as described above, the track pitch is  $0.32\mu\text{m}$  or more and mark length is  $0.12\mu\text{m}$  or more. The track pitch is  $0.38\mu\text{m}$  or less and the mark length is  $0.22\mu\text{m}$  or less. An optical pickup 2 for reading information comprises a laser beam source (not shown in Fig.1) having wavelength of 390 to 440 nm, a photo detector (PD) for receiving reflected light (not shown in Fig.1), and an amplifying means for amplifying signals (not shown in Fig.1). A description of a mechanical portion and a control portion for the disc and pickup operation is omitted in Fig.1 for simplification of the description.

[0016]  $0.22\mu\text{m}$  and  $0.38\mu\text{m}$  which are upper limits of the mark length and track pitch are specified as values allowing recording of information on an optical disc medium of 12 cm in diameter with practically sufficient capacity. On the other hand,  $0.12\mu\text{m}$  is a lower limit of the mark length and is specified as a value within its practical limit not to increase jitters rapidly due to thermal interference between adjacent marks. Further,  $0.32\mu\text{m}$  is a lower limit of the track pitch and is a value specified by manufacturing limit for disc.

[0017] A signal outputted from the pickup 2 is inputted to a DC block circuit 3, in which substantially DC component is removed and its amplitude is maintained constant by AGC 4. After that, sample estimation (resample) is carried out based on data point phase by a resampling DPLL 5. It is assumed that sampling output is carried out on a signal at a sampling frequency higher than bit clock at any point prior to input to resampling DPLL 5 from the PD. In the meantime, the resampling DPLL 5 corresponds to a digital phase synchronizing means.

[0018] Zero point information indicates a point where bit sampling data crosses zero level in bit clock unit. The resampling DPLL 5 operates to lock resampling timing, namely frequency and phase, so that resampling data value becomes zero at a phase  $180^\circ$  corresponding to zero cross point indicated by this zero point information. The resampling DPLL 5 outputs resample data and zero point information to a subsequent adaptive equalizing circuit 6 and generates a tap coefficient control signal for controlling tap coefficient of a transversal filter composing the adaptive equalizing circuit 6 so that resampling data sequence converges to a predetermined equalizing target set up by the adaptive equalizing circuit 6 based on the equalizing target and the zero point information. Because the transversal filter composing the adaptive equalizing circuit 6 will be described later, a representation thereof in Fig.1 is omitted.

[0019] The adaptive equalizing circuit 6 controls a coefficient of the transversal filter based on an error between a predetermined equalizing target and resampling sequence to be inputted, namely an equalizing error, and outputs the equalizing error to an estimation value generating circuit 7. Meanwhile, the adaptive equalizing circuit 6 is sometimes described as an automatic equalizing means or an adaptive waveform equalizing means.

[0020] A signal from which waveform distortion is removed by the adaptive equalizing circuit 6 is decoded by a viterbi decoder 8 to binary data. In this decoding operation, like the well known viterbi decoding, branch metric and path metric are computed based on the estimation value (viterbi target value) and inputted sample, and path memory is controlled by the result of such computation so as to decode to binary data. In an embodiment shown in Fig.1, for example, input of an estimation value may be inputted from outside.

[0021] Data binarized by the viterbi decoder 8 is subject to synchronism detection, data demodulation, descramble, ECC and the like by the subsequent signal processing circuit 9. The binarized data processed by the subsequent signal processing circuit 9 is outputted as reproduction data by an information processing means (not shown in Fig.1).

[0022] Next, each structure shown in Fig.1 will be described further in detail. Fig.2 is a block diagram showing an example of circuit configuration of the adaptive equalizing circuit 6 according to the first embodiment.

[0023] The adaptive equalizing circuit 6 comprises a transversal filter 61 for providing resample data from the resampling DPLL 5 with PR equalizing characteristic, a multiplier/LPF (low pass filter) 66 for changing the coefficients of the transversal filter 61 depending on an error signal, a tap delay circuit 63 for delaying zero point information from the resampling DPLL 5, a temporary determining circuit 62 for generating an equalizing target based on output signal from the transversal filter 61 and a delay signal from the tap delay circuit 63, a subtractor 67 for generating an equalizing error from a difference an input of an equalizing target outputted from the temporary determining circuit 62 and another input of an waveform equalization output, an error selecting circuit 64 for generating an error signal for controlling the tap coefficients of the transversal filter 61 based on the equalizing target outputted by the temporary determining circuit 62 and the equalizing error generated by the subtractor 67, and an inverter (INV) 65 for inverting the polarity of the error signal outputted from the error selecting circuit 64 and supplying the inverted signal to the multiplier/LPF 66.

[0024] The resampling data inputted to the adaptive equalizing circuit 6 from the resampling DPLL 5 has been eliminated waveform distortion adaptively by the transversal filter 61 whose coefficient is variable and then outputted to the viterbi decoder 8. Although the coefficients of the transversal filter 61 can be controlled by

a well known algorithm such as least mean square (LMS) method, zero focusing (ZF) method and the like based on the equalizing error, i.e. a difference between the equalizing target and an output after the waveform is equalized. According to the present invention, the equalizing error is generated by the error selecting circuit 64 based on the equalizing target outputted by the temporary determining circuit 62 and the equalizing error outputted by the subtractor 67.

**[0025]** Fig. 3 is a diagram for explaining temporary determining operation and error selecting operation. Although according to this embodiment, RLL(2, k) of run length restriction and PR (a, b, b, a) of partial response characteristic will be described as  $a=b=1$ , namely, poly binary PR(1, 1, 1, 1), it is needless to say that this embodiment can be applied to other run length restriction and other partial response characteristic.

**[0026]** A waveform equalization output from the transversal filter 61 and zero point information generated by the resampling DPLL 5 are inputted to the temporary determining circuit 62 through the tap delay circuit 63. The resample DPLL 5 detects a data point phase based on the phase crossing the zero level and is capable of outputting zero point information or a phase point in which input sample crosses the zero level. It is assumed that the zero point information possesses 1 at a data point estimated to be zero level.

**[0027]** Zero point information is inputted to the tap delay circuit 63 driven by bit clock and its timing configures with a timing of waveform equalization output sample  $y(k)$ . There provided five outputs come from the tap delay circuit 63 so that the equalizing target can be estimated from a center point, namely the third bit, of the five points of zero point information.

**[0028]** Referring to an example shown in Fig. 3, the estimation value is 0' ( $x'$ ) indicates the estimation value here) in case of 00100 (case a shown in Fig. 3), the estimation value is 1' in case where sample  $y(k)$  is positive in 01000 (case b shown in Fig. 3), the estimation value is -1' in case where  $y(k)$  is negative in 10010 (case c shown in Fig. 3), the estimation value is 2' in case where  $y(k)$  is positive in 10001 (case d shown in Fig. 3)

**[0029]** The temporary determining circuit 62 outputs the estimation value as the equalizing target, and assuming that the estimation value at  $k$  sample is  $x(k)$ , the equalizing error in the subtractor 67 is a difference between the waveform equalization output and the equalizing target, namely  $y(k)-x(k)$ . The temporary determining circuit 62 can be composed of a logical circuit and the like.

**[0030]** The error selecting circuit 64 operates to select an equalizing error for use in correcting the coefficient of the transversal filter 61 from equalizing errors generated by the subtractor 67. For example, in case of reproduced waveform poor in symmetry because of non-linear distortion caused by the aforementioned pit formation problem, only equalizing errors corresponding to the equalizing targets 1', 0', -1' having relatively small

distortion are selected to correct the tap coefficient. Then, it is possible to determine and select an equalizing error based on an equalizing target outputted from the temporary determining circuit 62. Instead, it is possible to determine in advance which error should be selected or it is possible to monitor signal quality with another circuit and determine an equalizing error depending on its result.

**[0031]** Fig. 4 is a block diagram of an example of a concrete circuit configuration of the estimation value generating circuit 7 and the viterbi decoder 8. The estimation value generating circuit 7 comprises a subtracting circuit 71 for the equalizing target and equalizing error, a LPF (low pass filter) 72 for averaging the output of the subtracting circuit 71 for each equalizing target (although five target values are provided in this example, apparently the number of the equalizing target value changes according to other partial response method), and an arithmetic operation circuit 73 for generating an estimation value (indicating  $tu, mu, o, ml, tl$  in the same Figure) to be given to the viterbi decoding circuit using the output of the LPF for each equalizing target. It is needless to say that the subtracting circuit 71 may act as the subtractor 67 at the same time.

**[0032]** The difference between the equalizing target and the equalizing error is a sample outputted actually from the adaptive equalizing circuit 6 and then, sample average values (indicated by  $TU^{\wedge}, MU^{\wedge}, O^{\wedge}, ML^{\wedge}, TL^{\wedge}$ ) are obtained by averaging the samples for each equalizing target.

**[0033]** After that, if the symmetry is poor so that convergence of sample corresponding to outside samples ( $TU^{\wedge}, TL^{\wedge}$ ) is poor, as an example, an arithmetic operation like  $tu = -tl = (TU^{\wedge} - TL^{\wedge})/2$  is carried out so as to correct two outside viterbi estimation values and then outputted to the viterbi decoder 8.

**[0034]** The viterbi decoder 8 carries out branch metric operation with the branch metric operation circuit 81 based on the corrected viterbi estimation values and carries out path metric operation with the path metric operation circuit 82 based on branch metric obtained by the branch metric operation circuit 81, controls the path memory with remaining path memory portion 83 and outputs decoded data. A viterbi estimation value in an initial state before correction is set up to, for example, an equalizing target.

**[0035]** Fig. 5 is an example of histogram of data point samples obtained when reproducing a ROM disc formed with track pitch of  $0.341\mu m$  and shortest mark length of  $0.22\mu m$  in wavelength of 405 nm or a sample histogram at the time when the coefficient of the transversal filter is corrected by using the equalizing errors of all waveform equalization output samples having poor symmetry in waveform without selecting the error. Fig. 6 is a histogram obtained at the time of tap correction by selecting only the equalizing error for samples corresponding to three values, 1, 0, -1. The axis of ordinate in Figs. 5, 6 indicates the quantity of samples and the

axis of abscissa indicates the estimation value of the viterbi decoder in case where the arithmetic operation mentioned in the above embodiment is carried out.

**[0036]** Although in Fig. 5, the waveform distortion is large and convergence is poor at all sample points, particularly convergence is very poor at outside sample, it is obvious that the convergence is improved at the time of error selection in Fig. 6.

**[0037]** Figs. 7, 8 are diagrams showing a tilt margin of the embodiment of the present invention. Measured values according to a conventional example and the present invention are shown about radial tilt (Fig. 7) and tangential tilt (Fig. 8) respectively. According to the present invention, the tilt margin becomes large in any case, so that its effect is obvious. By constructing an optical disc reproducing apparatus with a means capable of obtaining an equalized waveform in a good state even if the recording mark dimension and track pitch are small, a viterbi decoding means in which the estimation value can be set up from outside, and a means for maintaining the estimation value to be given to the viterbi decoder from the means for obtaining the equalized waveform constantly in a good condition, as described above, signals can be reproduced in a good condition from an optical disc having track pitch of  $0.32\mu\text{m}$  or more to less than  $0.38\mu\text{m}$  and mark length of  $0.12\mu\text{m}$  to less than  $0.22\mu\text{m}$ , which dimensions had not been allowing signals to be reproduced properly. As a result, the influence of nonlinear distortion caused by pit formation in a high recording density ROM or rewritable disc, cross-talk between adjacent tracks, disc tilt and the like can be minimized.

**[0038]** Next, a second embodiment of the present invention will be described. According to the second embodiment, the pickup 2 shown in Fig. 1 has at least another second reflected light detecting means in addition to the PD which is the first reflected light detecting means. In subsequent circuit configuration up to the transversal filter also, plural circuits are provided in parallel to each other.

**[0039]** Fig. 9 is a diagram showing a second embodiment of the present invention. Although this example shows a waveform equalizing circuit for using three-divided emission light or so-called three beams, this is applicable for other number of beams.

**[0040]** In Fig. 9, a signal obtained from main beam output is inputted to input 1 and signals obtained from two sub-beams (preceding beam, following beam) are inputted to inputs 2, 3. Here, the sub-beam signals are input signals from tracks adjacent to the main beam. A resampling DPLL 100 is connected to the input 1 so as to estimate data point phase and data point sample. After this, the signal is inputted to a delay circuit 101, which is provided to absorb a time difference between the preceding beam input 2 and following beam input 3 with respect to the main beam input 1. A subsequent operation is equal to the above described adaptive equalizing circuit 6 and therefore a description thereof is omitted.

**[0041]** Data point phase and bit clock are outputted from the resampling DPLL 100 to the resampling circuits 200, 300 corresponding to the inputs 2, 3, respectively. Based on the bit clock and data point phase signal, signal sample in data point phase of the main beam is obtained from the inputs 2, 3. The delay circuits 201, 301 corresponding to the inputs 2, 3 respectively are circuits for time delay so as to obtain a sample having the same phase as the sample of the input 1.

**[0042]** An operation of the second embodiment will be described with reference to Figs. 10A to 10C. Fig. 10A shows a cross-talk component mixed in a signal outputted from the main beam. The cross-talk component affects the main beam signal as a nonlinear distortion and cannot be removed by waveform equalization for the main beam of the input 1. As a result, the cross-talk component is multiplexed as remaining error as shown in Fig. 10B, so that the error is increased.

**[0043]** The sub-beam input signal inputted to the inputs 2, 3 are a factor of the cross-talk, which is shown in Fig. 10C. The remaining error at data point obtained from the main beam signal i.e. the cross-talk component, can be detected as a pseudo cross-talk in a loop containing transversal filters 202, 302 connected to the inputs 2, 3 of Fig. 9 and multiplier/LPFs 203, 303. Consequently, a signal from which the cross-talk is removed can be detected by reducing the pseudo cross-talk from the main beam waveform equalization output with the subtractors 104, 105.

**[0044]** That is, although as regards the coefficients of the transversal filters 202, 302, negative feedback loop is so constructed that an error outputted from the error selecting circuit 106 is to be small, of errors outputted from the error selecting circuit 106, an error caused by linear distortion due to simple interference between codes is corrected by main beam waveform equalizing loop and an equalizing error which cannot be corrected remains as remaining error (Fig. 10A).

**[0045]** The remaining error acts as an equalizing error signal (remaining equalizing error) to sub-beam loop. The remaining equalizing error is generated as pseudo cross-talk component by a circuit means comprising the transversal filters 202, 302 connected to the sub-beams and the multiplier/LPFs 203, 303 respectively, and is removed by the subtractors 104, 105 so that the remaining equalizing error can be small.

**[0046]** Because the error selecting circuit 106, the temporary determining circuit 107, the tap delay circuit 108, the inverter (INV) 109, and the multiplier/LPF 103 shown in Fig. 9 are attached thereto for the same purpose of the first embodiment, error selection for the main beam as well as the sub-beam is enabled.

**[0047]** The circuit configuration and operation of the viterbi decoder 8 and estimation value generating circuit 7 connected to the adaptive equalizing circuit according to the second embodiment are the same as the viterbi decoder 8 and estimation value generating circuit 7 described in the first embodiment and therefore, a descrip-

tion thereof is omitted respectively. As described above, according to the second embodiment, a preferable reproduced signal can be obtained for a signal having a large amount of cross-talks.

[0048] In the optical reproducing apparatus of the present invention, reproduction from the high density ROM or rewritable disc, which is conventionally difficult, can be carried out preferably and further, there is an advantage that system margin such as tilt margin and recording power margin is increased.

[0049] It should be understood that many modifications and adaptations of the invention will become apparent to those skilled in the art and it is intended to encompass such obvious modifications and changes in the scope of the claims appended hereto.

### Claims

1. An optical disc reproducing apparatus for reproducing digital information from an optical disc medium (1) in which the information is modulated and recorded in a recording track formed coaxially or spirally according to a signal modulation method for restricting run length such that a recording mark is 0.32 $\mu$ m or more to less than 0.38 $\mu$ m in track pitch and 0.12 $\mu$ m or more to less than 0.22 $\mu$ m in mark length, comprising:

laser beam source (2) for emitting laser beam having wavelength of 390 to 440 nm;  
a reflection light detecting means (2) for detecting a reflection light produced when the laser beam emitted from the laser beam source is reflected by the optical disc medium (1) to read a signal from the optical disc medium (1), so as to reproduce the information recorded in the optical disc medium (1);  
a digital phase synchronizing means (5) for receiving a supply of signal outputted from the reflection light detecting means (2) and digitally synchronizing phase so as to detect a phase at a data point phase and estimate digital data at the data point and outputting the digital data;  
an adaptive equalizing means (6) including a transversal filter (61) for equalizing in terms of waveform, the digital data outputted from the digital phase synchronizing means (5) based on a filter coefficient, a temporary determining/ subtracting circuit (62, 67) for while a sample after the waveform equalization outputted from the transversal filter (61) is inputted therein, computing a temporary determining value for partial response equalization by comparing the sample with at least one threshold and outputting a differential between the temporary determining value and the sample after the waveform equalization as an error signal, and a co-

efficient control circuit (64, 66) for variably controlling a tap coefficient of the transversal filter (61) so that the error signal becomes minimum; and

a viterbi decoding means (8) for viterbi-decoding a sample outputted from the adaptive equalizing means (6).

2. An optical disc reproducing apparatus according to claim 1 wherein the coefficient control circuit (64, 66) includes an error selecting circuit (64) for while the error signal and the temporary determining value outputted from the temporary determining means (62) are inputted therein, selecting and outputting only valid component of the error signal depending on the temporary determining value, and a multiplier/low-pass-filter (66) for variably controlling the tap coefficient of the transversal filter (61) based on a signal outputted from the error selecting means (64) such that the error signal becomes minimum.
3. An optical disc reproducing apparatus according to claim 1 further comprising an estimation value generating means (7) for generating an estimation value by averaging output sample of the transversal filter (61) for each of the at least one threshold based on the temporary determining value and the error signal outputted from the temporary determining/ subtracting circuit (62, 67).
4. An optical disc reproducing apparatus according to claim 2 wherein the viterbi decoding means (8) includes an input means for an estimation value for use in metric operation, the estimation value being capable of being controlled through the input means.
5. An optical disc reproducing apparatus according to claim 4 wherein the estimation value for the viterbi decoding means (8) is provided by the estimation value generating means (7).
6. An optical disc reproducing apparatus for reproducing digital information from an optical disc medium (1) in which the information is modulated and recorded in a recording track formed coaxially or spirally according to a signal modulation method for restricting run length such that a recording mark is 0.32 $\mu$ m or more to less than 0.38 $\mu$ m in track pitch and 0.12  $\mu$ m or more to less than 0.22  $\mu$ m in mark length, comprising:

laser beam source (2) for emitting laser beam having wavelength of 390 to 440 nm;  
a first reflection light detecting means (2) for detecting a reflection light produced when the laser beam emitted from the laser beam source (2) is reflected by the optical disc medium (1)

to read a signal from the optical disc medium (1), so as to reproduce the information recorded in the optical disc medium (1);

a second reflection light detecting means (2) for reading information separately from each of at least one pair of recording tracks on both sides of the recording track;

a digital phase synchronizing means (100) for while a signal outputted from the first reflection light detecting means (2) is supplied thereto, digitally synchronizing phase so as to detect a phase at a data point and estimate digital data at the data point and outputting the digital data; a resampling means (200:300) for while a signal outputted from the second reflection light detecting means (2) is supplied thereto, resampling a signal from at least one recording track of the at least one pair of the recording tracks at the data point in the digital phase synchronizing means (100);

an adaptive equalizing means including a first transversal filter (102) for equalizing in terms of waveform, the digital data outputted from the digital phase synchronizing means (100) based on a first filter coefficient, a temporary determining/subtracting circuit (107, 110) for while a sample after the waveform equalization outputted from the first transversal filter (102) is inputted therein, computing a temporary determining value for partial response equalization by comparing the sample with at least one threshold and outputting a differential between the temporary determining value and the sample after the waveform equalization as an error signal, a second transversal filter (202:302) for equalizing in waveform, the digital data outputted from the resampling means (200:300) based on a second filter coefficient and generating a pseudo cross-talk signal based on an inputted tap coefficient, a subtracting means (104:105) for subtracting the pseudo cross-talk signal from the sample after the waveform equalization, and a coefficient control circuit (106, 103, 203:303) for variably controlling the tap coefficient inputted to the first and second transversal filters (102, 202:302) so that the error signal becomes minimum; and a viterbi decoding means (8) for viterbi-decoding a sample outputted from the adaptive equalizing means.

7. An optical disc reproducing apparatus according to claim 6 wherein the coefficient control circuit (106, 103, 203:303) includes an error selecting circuit (106) for while the error signal and the temporary determining value outputted from the temporary determining means (107) are inputted therein, selecting and outputting only valid component of the error

signal depending on the temporary determining value, and a multiplier/low-pass-filter (103, 203:303) for variably controlling the tap coefficient of the first and second transversal filters (102, 202:302) based on a signal outputted from the error selecting means (106) such that the error signal becomes minimum.

8. An optical disc reproducing apparatus according to claim 6 further comprising an estimation value generating means (7) for generating an estimation value by averaging output sample of the first and second transversal filters (102, 202:302) for each of the at least one threshold based on the temporary determining value and the error signal outputted from the temporary determining/subtracting circuit (107, 110).
9. An optical disc reproducing apparatus according to claim 7 wherein the viterbi decoding means (8) includes an input means for an estimation value for use in metric operation, the estimation value being capable of being controlled through the input means.
10. An optical disc reproducing apparatus according to claim 9 wherein the estimation value for the viterbi decoding means (8) is provided by the estimation value generating means (7).



FIG.1

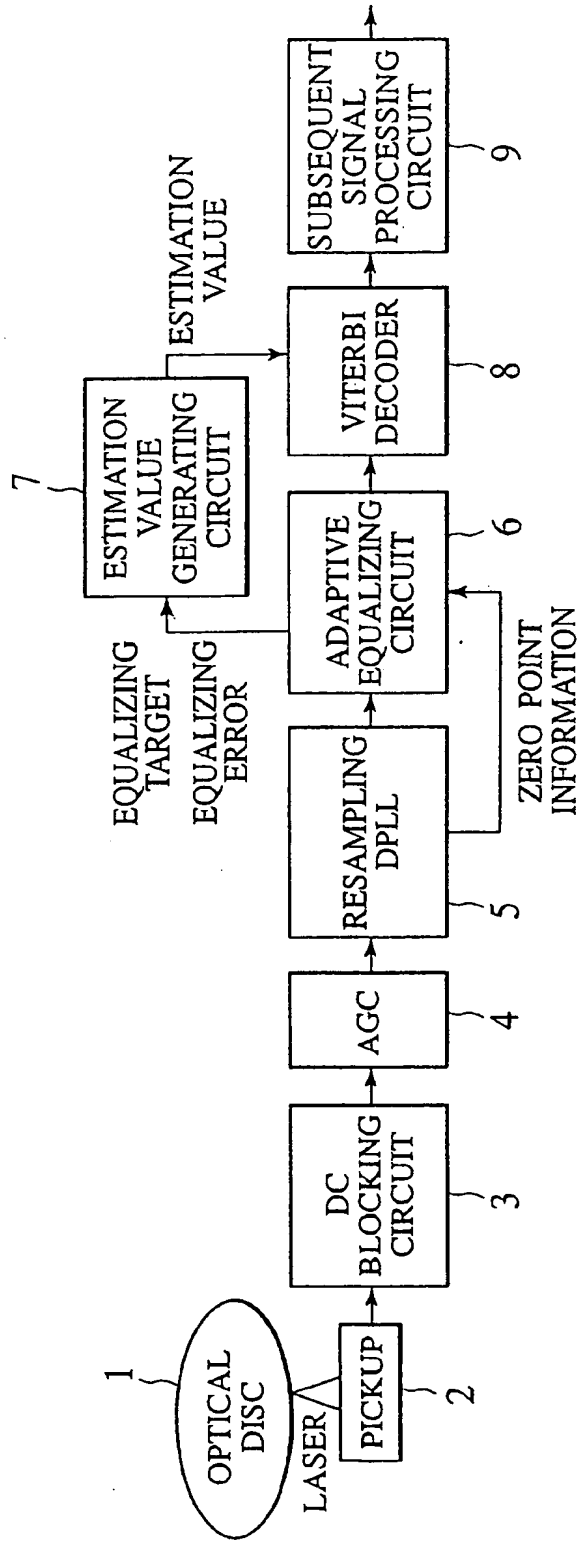


FIG.2

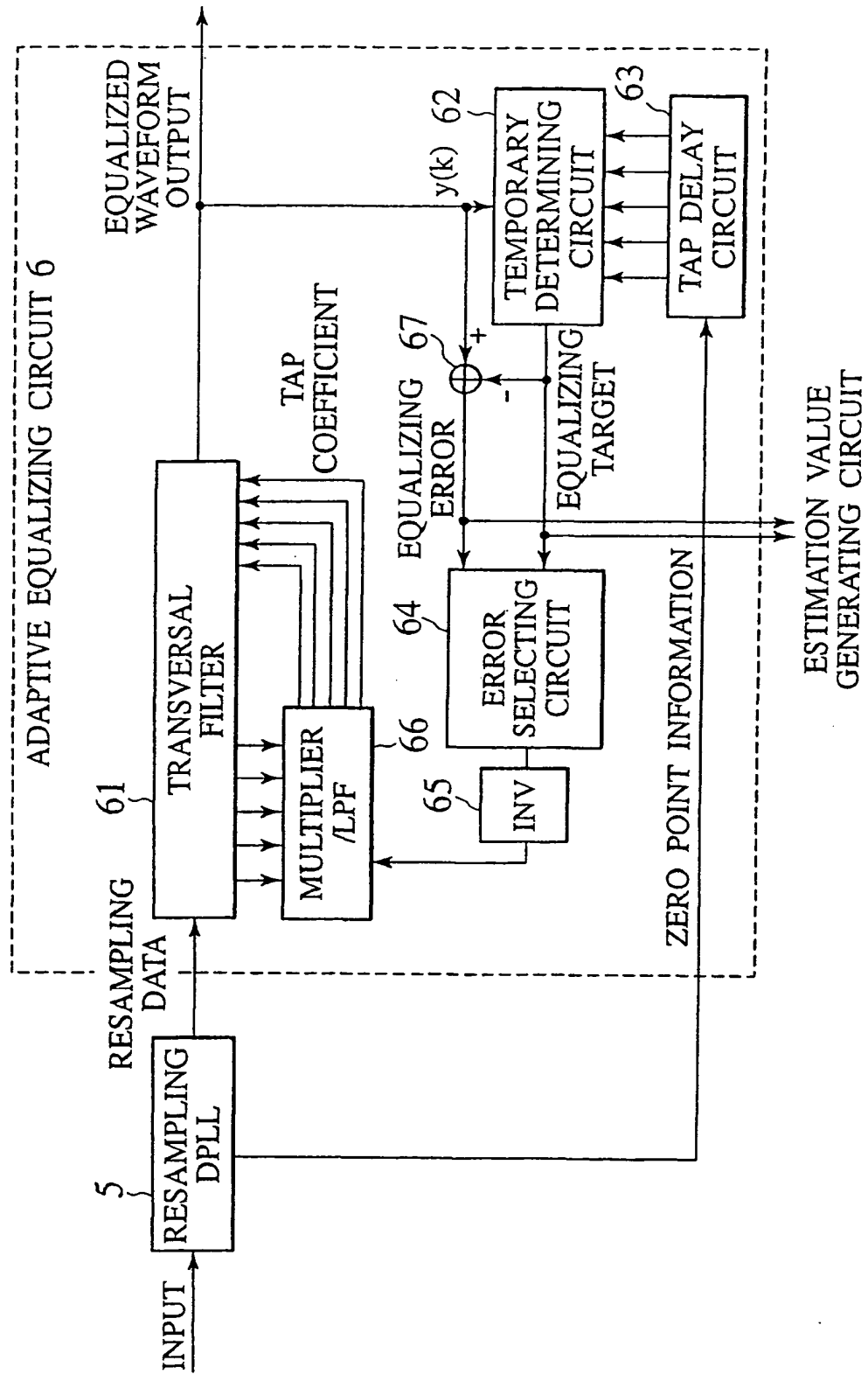


FIG.3

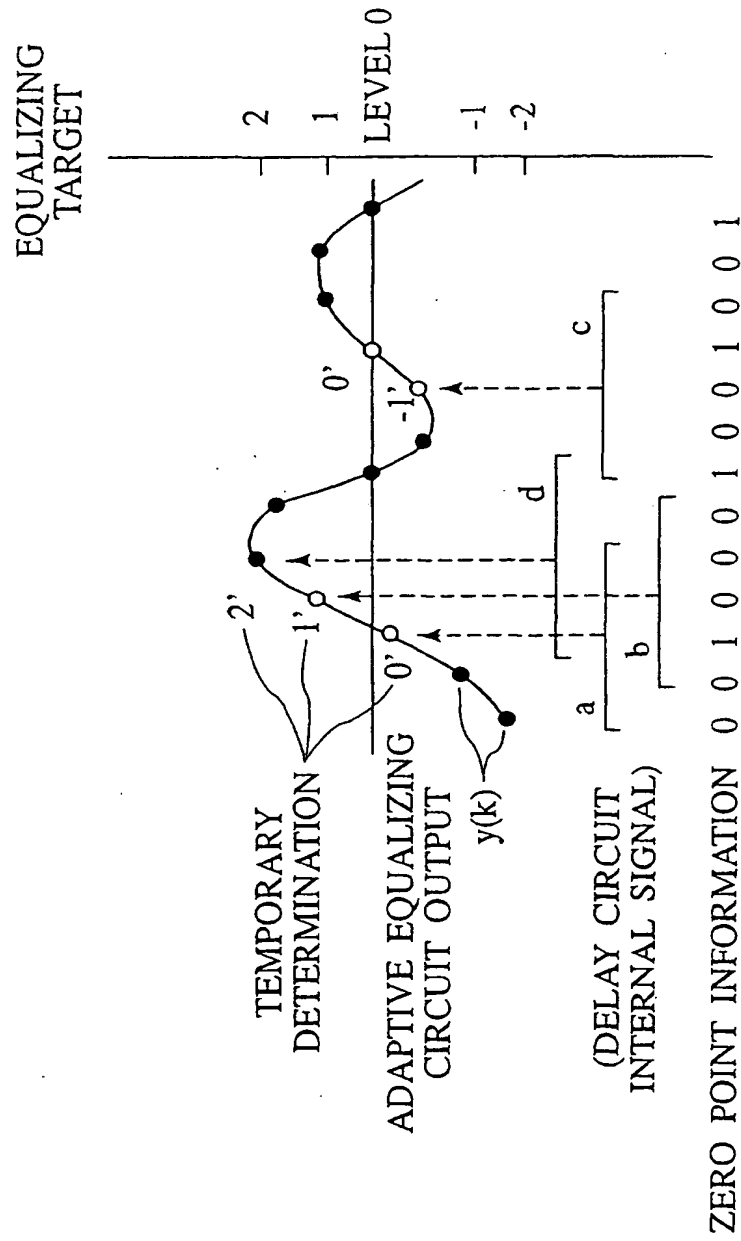
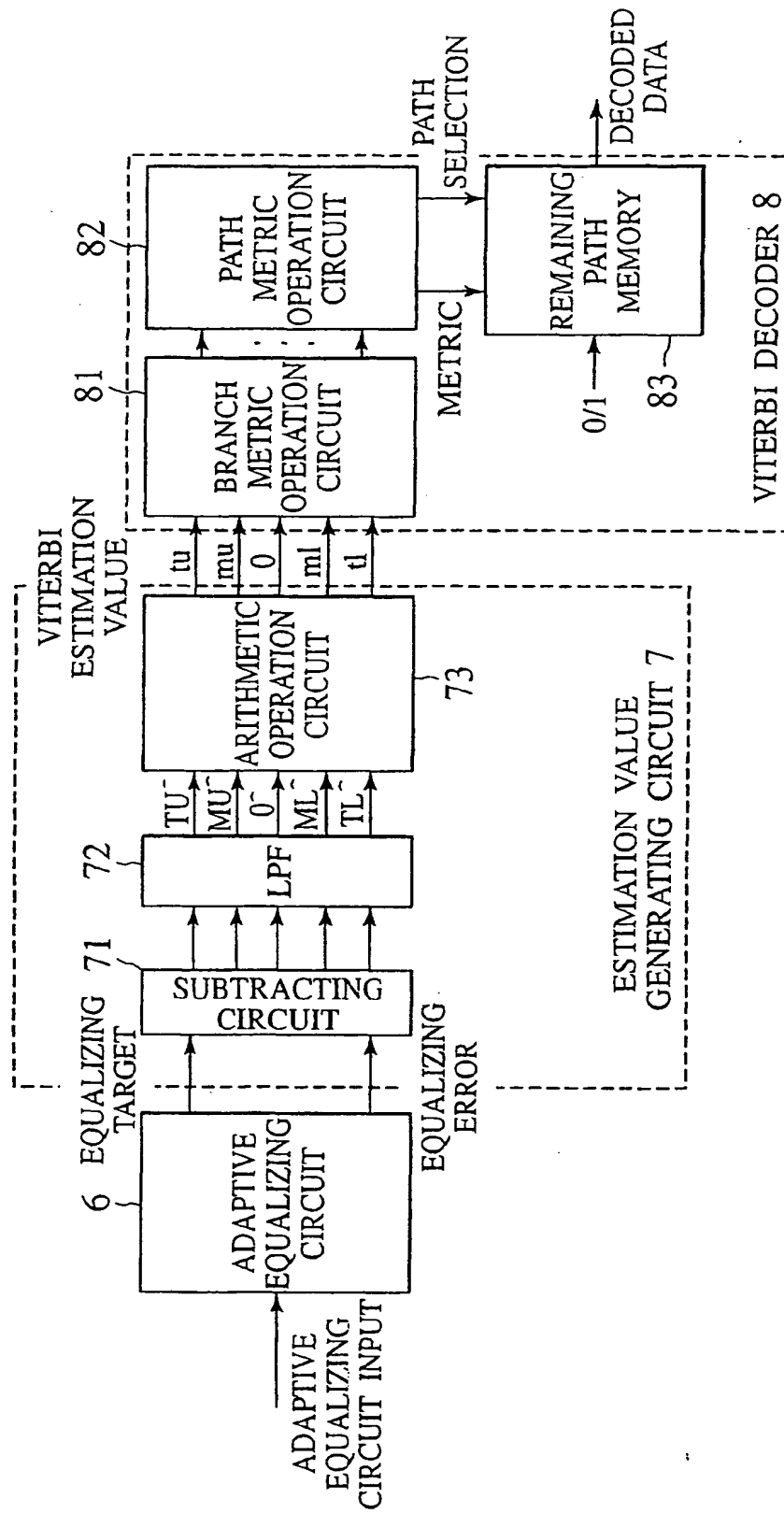


FIG. 4



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FIG.5

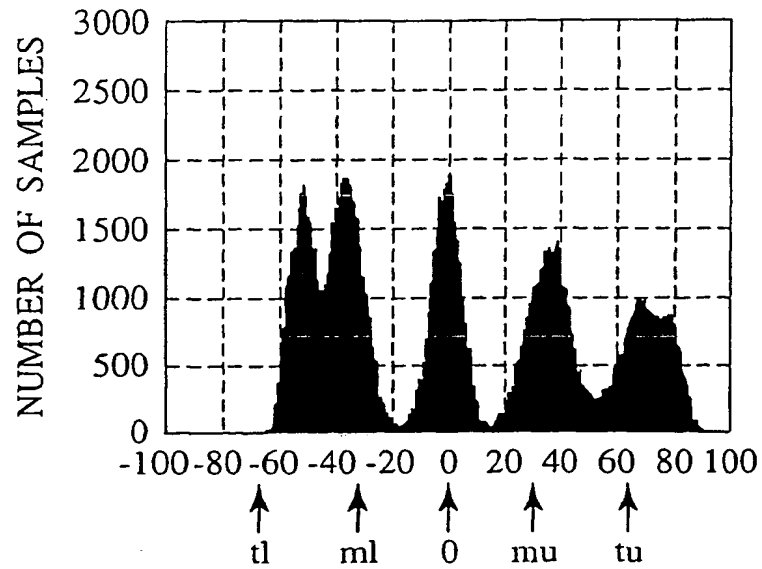


FIG.6

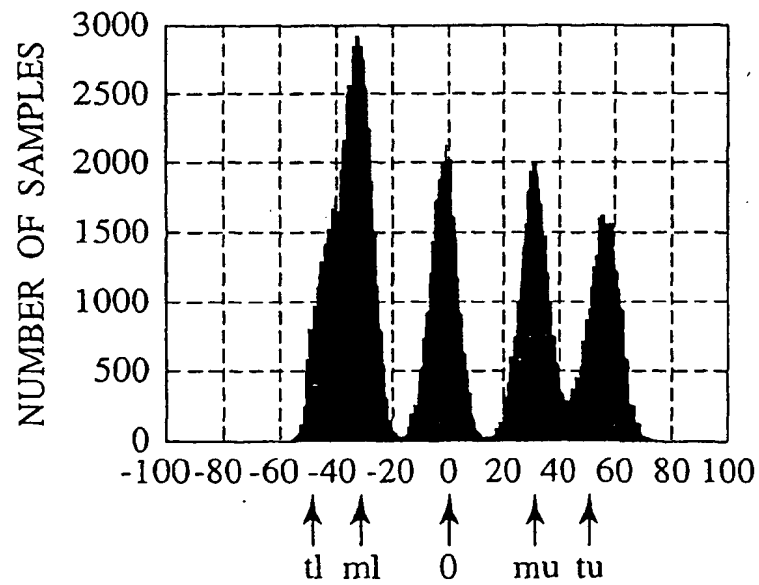


FIG.7

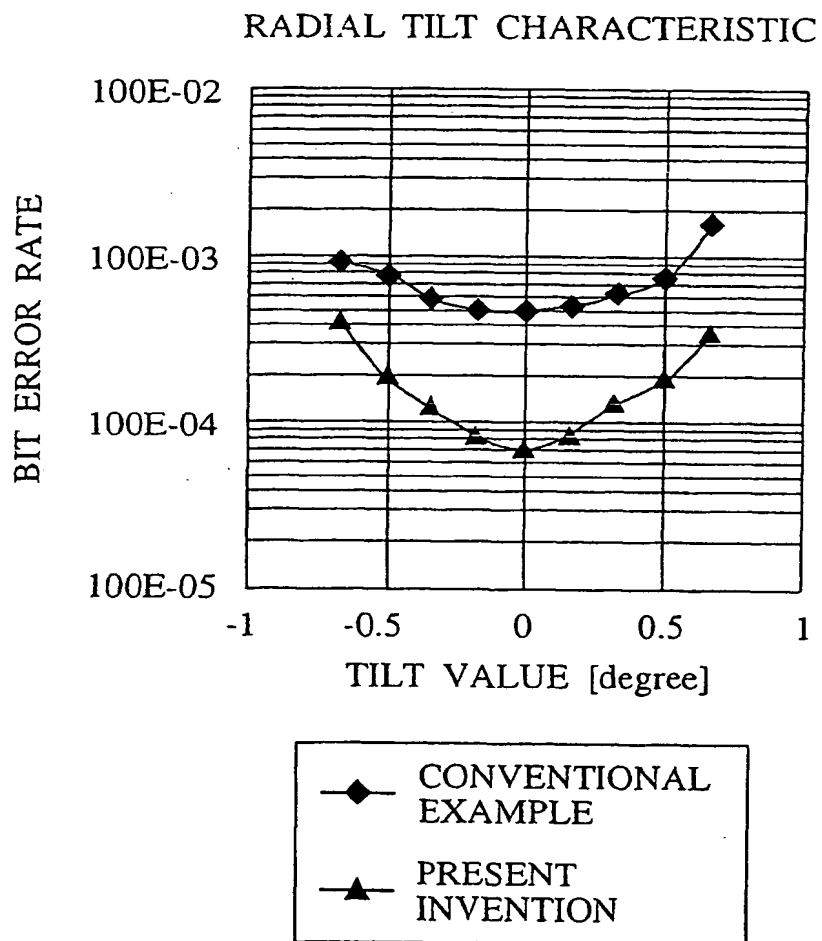


FIG.8

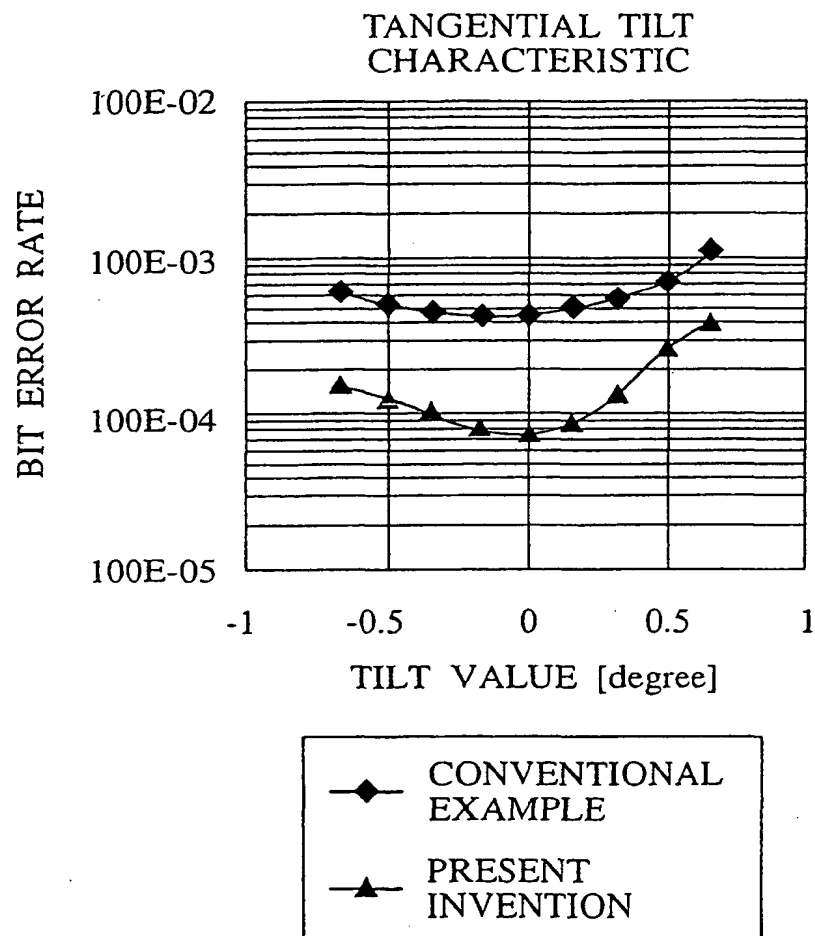


FIG. 9

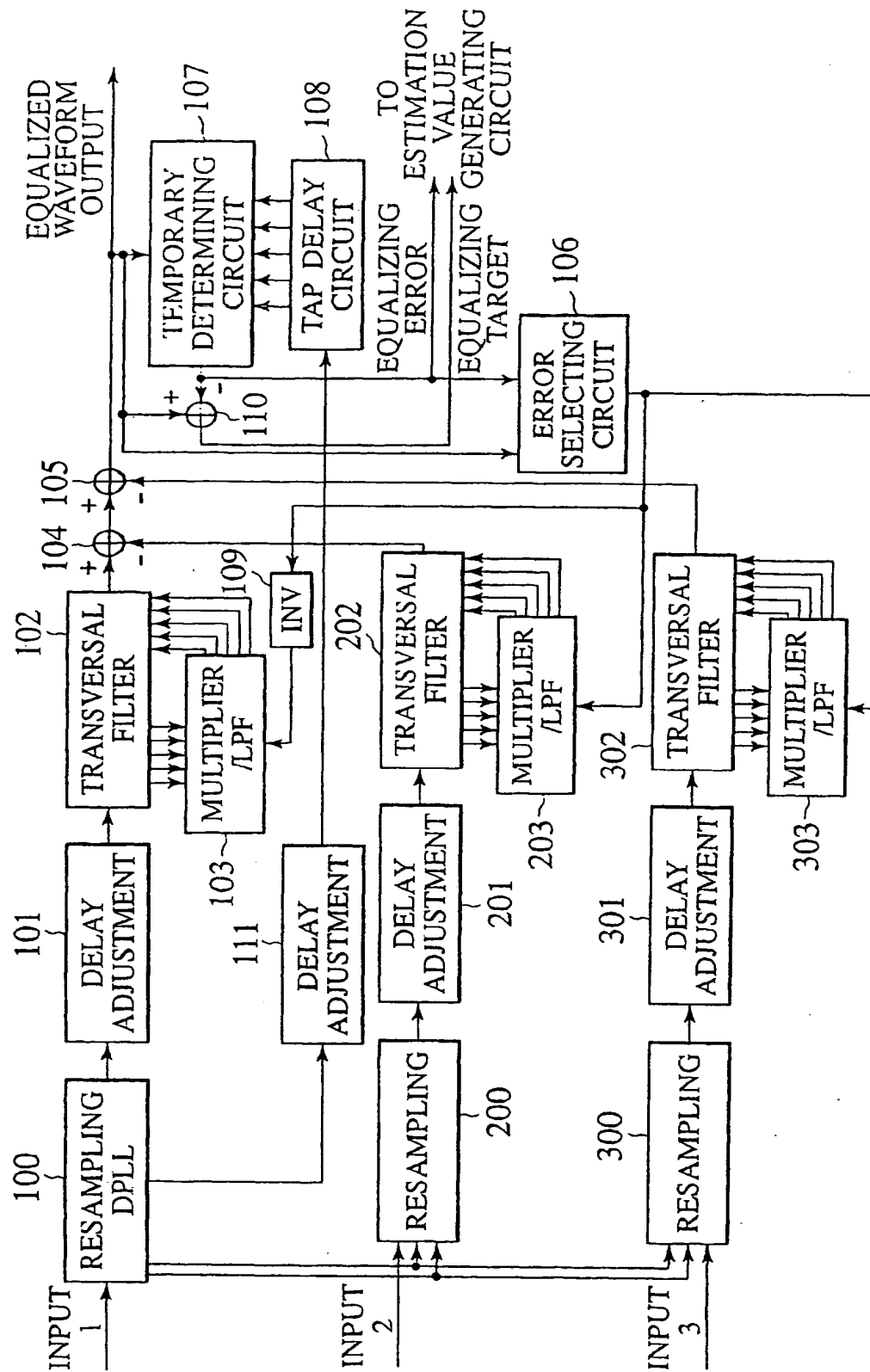




FIG.10A

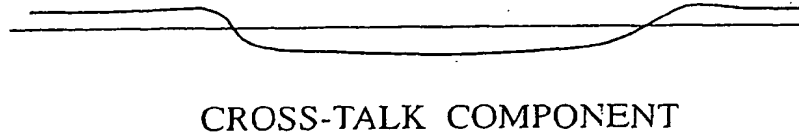


FIG.10B

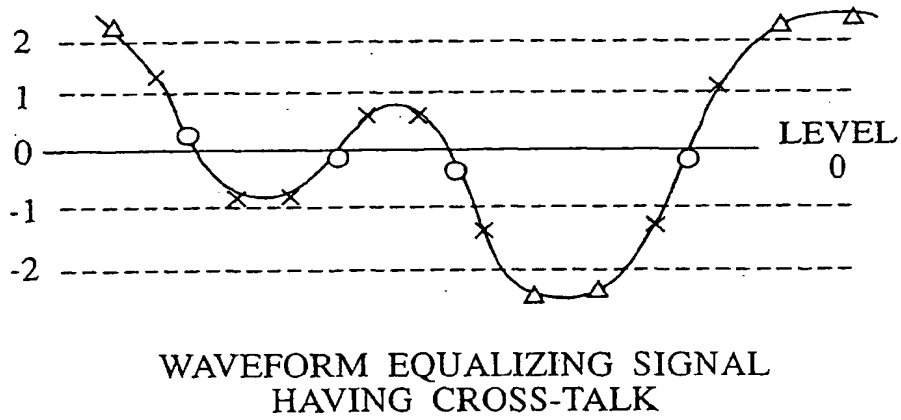
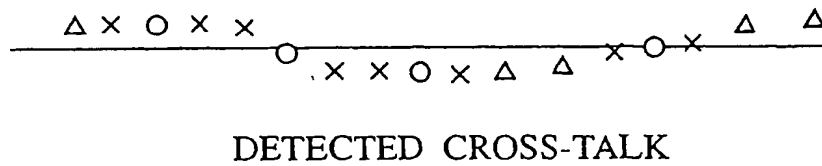


FIG.10C



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